

# Abstracts

## Current dumping: a novel linearisation technique for RF power transistors

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*P. Sandhiya, C.M. Snowden, R.D. Pollard and R.G. Ranson. "Current dumping: a novel linearisation technique for RF power transistors." 1999 MTT-S International Microwave Symposium Digest 99.2 (1999 Vol. II [MWSYM]): 815-818 vol.2.*

A novel 1 GHz MMIC linearised RF power amplifier is presented. The current dumping linearisation technique achieves a minimum of 29 dB intermodulation suppression over a bandwidth of 600 MHz employing only passive lumped components and an error amplifier. This novel technology demonstrates low sensitivity to variations in circuit element values and enables a significant reduction in circuit size at RF frequencies.

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